

Version of Amendments With Markings to Show Changes Made

Paragraph beginning at page 8, line 16 has been amended as follows:

FIG. 1 illustrates a block diagram of a telecommunications system 100 which includes the accessory interface bus of the present invention. As shown in FIG. 1, the telecommunications system 100 includes a headset adapter base 110 coupled between a local telephone 120 and a telecommunications headset 130. The headset adapter base 110 transmits audio voice signals between the telecommunications headset 130 and the local telephone 120 by adjusting and amplifying frequency and audio level parameters of these signals. The process of coupling and communicating voice signals between the local telephone [110] 120 and the telecommunications headset 130, via the headset adapter base 110, is well known in the art and no further discussion with regard to such coupling or communication is included within this application.

Paragraph beginning at page 15, line 14 has been amended as follows:

Finally, the communications packet further includes a Command/Data [220] 240 portion which includes a command or data in response to a command/status request. The command may be directed to a particular accessory or it may be directed to multiple accessories. In a testing environment, in which the interface bus of the present invention is implemented in an adapter base as part of a test system, the contents of the Command/Data portion [220] 240 may be compared with anticipated or expected results in order to ensure proper operations of a headset accessory.

Paragraph beginning at page 20, line 13 has been amended as follows:

Under these circumstances, all accessories coupled to the interface bus are configured to respond to any communications packet which includes [this] either of these broadcast addresses. For example, all accessories will respond to the all accessories broadcast address, while all on-line indicators will respond to both the all accessories broadcast address and the all on-line indicators reserved broadcast address.

Paragraph beginning at page 30, line 6 has been amended as follows:

A specific example is herein provided for further illustration. In this specific example, it is assumed that a headset adapter has a preferred transmission rate of 4800 bps. If the adapter base were able to constantly [transmitted] transmit at exactly 4800 bps, then the rate bit of such an adapter would have an ideal bit width, T_{IDEAL} of 208.33 μ S. However, it is understood that due to minor variations, the adapter base may not always transmit at exactly the same rate. Accordingly, the actual width of each rate bit transmitted by the adapter base may vary somewhat. Moreover, the transmission rate of various accessories coupled to the accessory interface bus in this example will preferably be somewhat related to the transmission rate of the host adapter base. In this way, one very slow accessory is not coupled to the bus and will not slow down bus operations for all other accessories coupled to the bus; and, one very fast accessory is not coupled to the bus thereby causing transmission errors between the accessory and the adapter base.

Paragraph beginning at page 34, line 16 has been amended as follows:

FIG. 6 illustrates a flow diagram illustrating the preferred method of detecting bit values. As shown, the bi-directional signaling line is monitored and a rising edge is first detected 601. The voltage on the bi-directional signaling line of the interface bus is measured at some point in time, T_x , from the rising edge 602, where T_x is approximately equal to T_{II} of a rate bit. If the voltage on the bi-directional signaling line of the interface bus at the time T_H 603 is still high, the bit is assigned a bit value of one 604. However, if the voltage on the bi-directional signaling line of the interface bus at the time T_H 603 has gone low, then the bit is assigned a bit value of zero 605.

Paragraph beginning at page 59, line 12 has been amended as follows:

Now that some of the common accessory commands have been set forth, the commands specified in the following section are specific to a particular accessory. These commands, coupled with the common commands previously set forth make up an entire command set for each accessory. Accessory specific commands may be directed toward all like accessories, where more than one of the same type is coupled to the interface bus, or the accessory specific commands may be directed toward a specific instance of an accessory. Thus, for example, if more than one on-line indicator (OLI) is coupled to the interface bus, an accessory specific command may be directed to all OLI's on the bus (using a common broadcast type address which is assigned to all OLI's) or an accessory specific command may be [directed] directed to just one particular OLI. In such a case, all OLI's will be programmed to recognize and respond to at least three addresses; the broadcast address for all devices (as used in a polling command for example), the broadcast address for all OLI's, and [th] the particular address assigned to that OLI.

Paragraph beginning at page 68, line 7 has been amended as follows:

This command instructs the OLI to move to the previous pattern in [it] its sequence. If the OLI is currently at the first pattern in its sequence, this command causes it to wrap around to the last pattern. The command has the following format:

CONCLUSION

Applicants believe that the Amendment filed on January 22, 2003 is in compliance with Rule 1.121 and respectfully request that the Amendment be entered and the Remarks therein be considered.

No fee is believed to be due. However, if the Patent Office determines that an Extension of Time under 37 CFR 1.136 and/or any other relief is required, Applicant hereby petitions for any required relief including Extensions of Time and/or any other relief and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 50-2315 (Order No. 01-3876).

Respectfully submitted,



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